

Docket No. 252069US2

IN RE APPLICATION OF: Hideki TAKAHASHI, et al.

SERIAL NO: 10/826,391

FILED: April 19, 2004

FOR: INSULATED GATE BIPOLAR TRANSISTOR WITH BUILT-IN FREEWHEELING DIODE

RESPONSE UNDER 37 C.F.R. 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 2815

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment under 37 C.F.R. § 1.116 in the above-identified application.

☒ No additional fee is required

☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	16	MINUS	20	0	x \$50 =	\$0.00
INDEPENDENT	3	MINUS	4	0	x \$210 =	\$0.00
APPLICATION SIZE		MINUS	100	0 (each addtl. 50 sheets)	x \$260 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$370 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		TOTAL				\$0.00

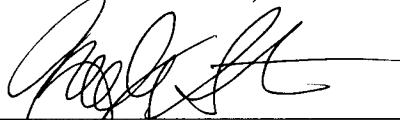
☐ A check in the amount of **\$0.00** is attached.

☐ Online credit card payment is being made to cover the fees in the amount of **\$0.00**

☒ Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030.

☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.

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